

Marathwada Mitra Mandal's
COLLEGE OF ENGINEERING, PUNE

An Autonomous Institute Affiliated to Savitribai Phule Pune University



EXAMINATION CIRCULAR NO. 250

PROGRAMME OF F.Y. (2024 PATTERN) (BACKLOG)

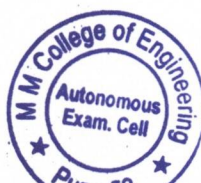
End Term Examination

Time Table

Nov/Dec-2025 Examination (Semester II) AY 2025-26

INSTRUCTIONS FOR CANDIDATES

- Candidates are required to be present at the examination hall, THIRTY MINUTES before the stipulated time.
- Candidates are forbidden from taking any material into the examination hall that can be treated as a malpractice.
- Candidates are requested to see the Notice Board at their center of examination regularly for changes if any that may be notified later in the program.
- No request shall be granted for change in time or date for the End Term Examination on any ground.
- Candidates are requested to note the Day, Date and Time of Paper.
- Candidates are permitted to use stencils at the time of examination.
- The exchanges of side-rules, drawing instruments of other materials used in the examination hall is not permitted at the time of examination. Candidates must bring their own instruments and will not be allowed to borrow from each other under any circumstances.
- Use of non-programmable battery-operated electronic pocket size Calculator is allowed. The exchange of Calculators is not allowed. Electronics Devices including mobile are not allowed at the time of examination.
- The written examination will be conducted in the following order.
- **Re-ETE is applicable only for Regular semester subjects. It is not applicable to any backlog subjects of previous semesters.**





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Examination Cell,
Karvenagar Pune- 411 052

Ref. No.: MMCOE/2025-26/Exam/250

Date: 12/11/2025

Backlog End Term Examination Time Table

Nov/Dec-2025 Examination (Semester II (Backlog)) AY 2025-26

First Year Master of Technology (FY M.Tech) Semester II (Backlog)

Time: 11:00 am - 02:00 pm (BN)

Programme	Course Code	Course Name	Day & Date
FY M.Tech. Computer Engineering Semester II	CE24PCC551	Advanced Databases	Wednesday 19.11.2025
	CE24PCC552	Software Architecture and Component-Based Design	Thursday 20.11.2025
	CE24PCC553	Natural Language Processing	Friday 21.11.2025
FY M.Tech. VLSI and Embedded Systems Semester II	ET24PCC551	Testing and Verification of VLSI Design	Wednesday 19.11.2025
	ET24PCC552	Embedded System in IOT	Thursday 20.11.2025
	ET24PCC553	Embedded Systems in Automation and Machine Learning	Friday 21.11.2025
FY M.Tech. Design Engineering Semester II	ME24PCC551	Finite Element Method in Design Engineering	Wednesday 19.11.2025
	ME24PCC552	Optimization Techniques in Design	Thursday 20.11.2025
	ME24PCC553	Mechanical Vibrations and Acoustics	Friday 21.11.2025
FY M.Tech. Power Electronics and Drives Semester II	EE24PCC551	Switched Mode Power Conversion	Wednesday 19.11.2025
	EE24PCC552	Special Machines	Thursday 20.11.2025
	EE24PCC553	Advanced Control Systems	Friday 21.11.2025

Controller of Examinations
MMCOE Pune

Principal
MMCOE Pune