

Marathwada Mitra Mandal's

**COLLEGE OF ENGINEERING, PUNE**

Accredited with 'A' Grade by NAAC, Recipient of " Best College Award 2019" by SPPU

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**Department of Electronics and Telecommunication Engineering**

**Date: 12/12/2022**

**Academic Year 2022-23, Sem-I**

**Report of Guest Lecture**

**Topic: Chip Design Life Cycle and Job Opportunities in VLSI**

**Date & Time : 10/12/2022, 10.30 am -11.30 am**

**Class : BE E&TC**

**No. of Students Present: 80**

**Co-ordinator for the guest lecture: Ms. M. R. Pangaonkar, Ms. H. N. Burande**

**Venue: Online - Google Meet**

**Speaker of Guest Lecture: Ms. Rucha Joshi**

**Designation and Experience: SoC Design Engineer, Intel Corporation, Bangalore**


**Topics covered in Guest Lecture:**

- 1) Chip Design Life Cycle
- 2) Job Opportunities in VLSI


**Outcome for Guest Lecture:**

Students are able to discuss-

- The Chip Design Life Cycle
- Scope of VLSI Design and Technology

  
Ms. M. R. Pangaonkar

Ms. H. N. Burande  
Course Coordinator

  
Dr. G. S. Gawande  
HOD-E&TC