

Department of Computer Engineering
Academic Year 2022-2023, Sem-II

Report of Innovative Teaching Pedagogy
Activity: Crossword

Date- 14/3/2023

Class: SE I & II

Subject : Microprocessor

Topic: 80386 Registers

Date & Time: 14/3/2023 Time: 10:00 AM to 11:00 AM

No. of Participants: 115/153

Mode of Conduction: Offline

Co-ordinator: Ms. Rupali Dalvi

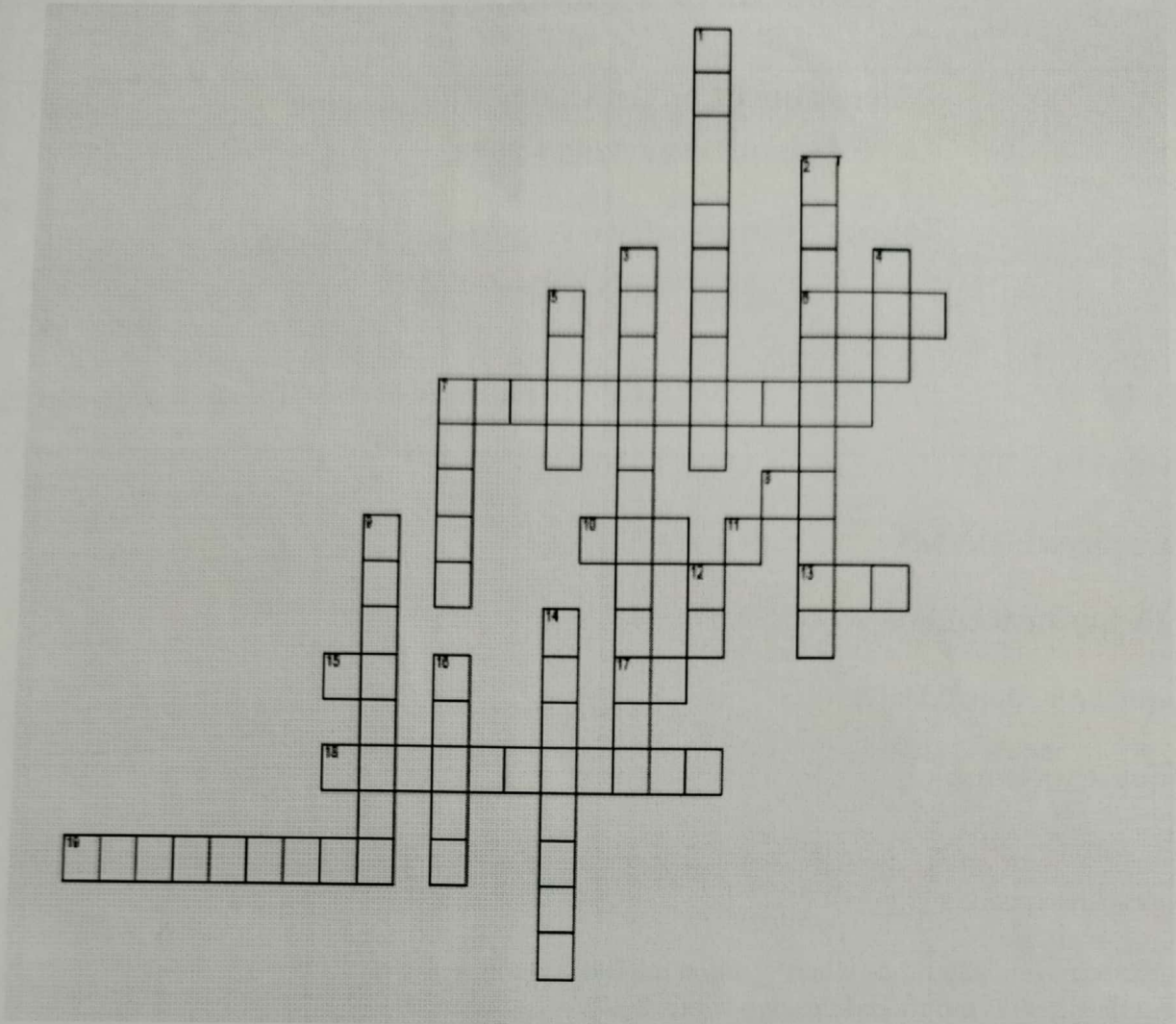
Way of conduction:

ACROSS:

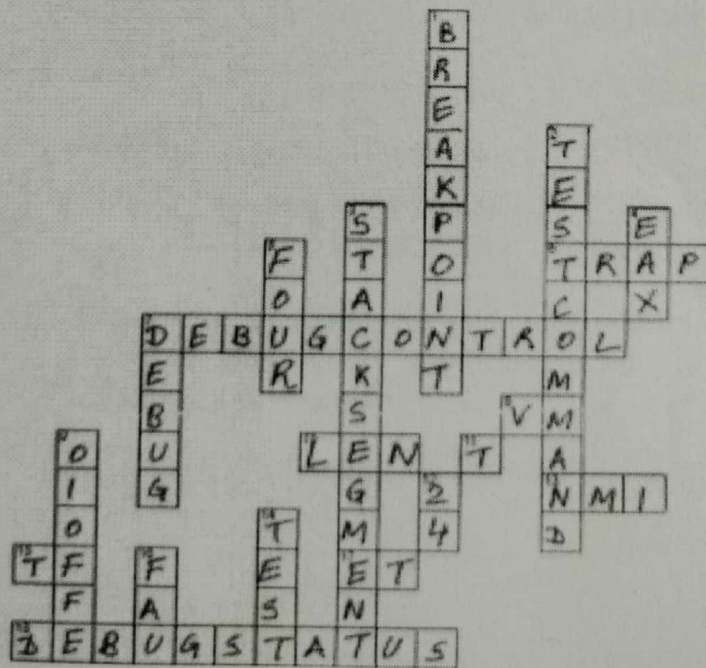
6. Data address breakpoint is ____
7. DR7 is ____ register.
8. Processor executes in V86 mode when __ bit in the EFLAG register is set.
10. Length of data item is monitored through which bits?
13. Interrupt table is accessed if an exception or __ occurs
15. The BS bit is associated with __ bit of EFLAGS.
17. Which bit of CR0 is set if an 80387 is present?
18. DR6 is ____ register.
19. First instruction begins at which physical address of 80386?

DOWN:

1. INT3 is __ exception
2. TR6 is __ register
3. Instruction cannot use stack unit ____ register has been loaded.
4. Which register indicates power on a self test is successful or not?
5. TLB is __ way to set associative memory?
7. INT 1 is __ exception 9. An 8086 program may generate linear addresses anywhere in the range 0 to ____
11. The BT bit is associated with __ bit of TSS.
12. Size of tags in TLB is __ bits
14. TR7 is __ register
16. Instruction address breakpoint is ____



Answer:

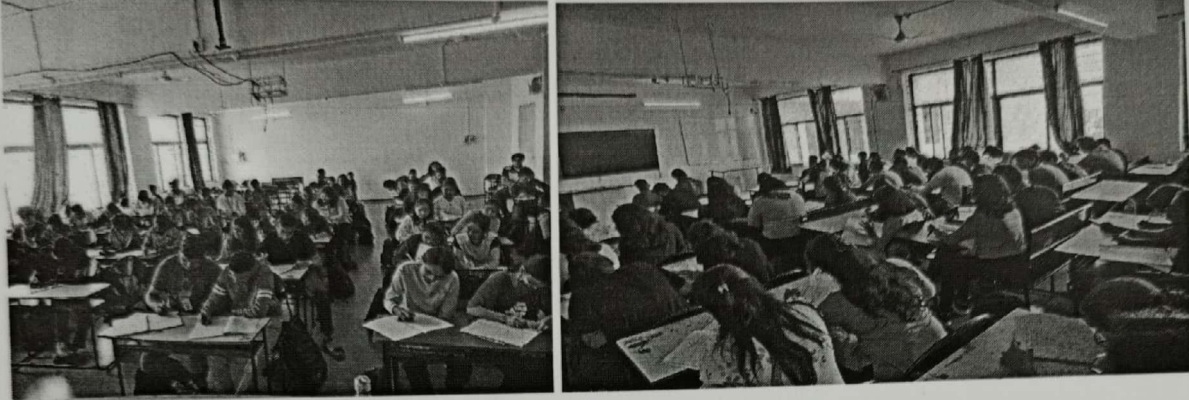


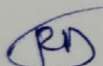
Outcome of the session:

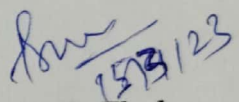
It helped students to

1. Understand the concept and remember the key terminologies of registers
2. Retain the concepts during examinations.
3. Enhances cognitive skills of learning

Glimpses of the Session:




Ms. Rupali Dalvi
Course Coordinator


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